

SISPAD 2026 Schedule at a Glance (as of August 28, 2026)

Sunday, September 27th	
9:30 - 12:40	Workshop Part A: Process Simulation
14:00 - 17:10	Workshop Part B: Transport Simulation

Monday, September 28th	
9:00 - 9:25	Opening (Main Hall)
9:25 - 11:40	Session PL: Plenary (Main Hall) 3 invited
11:40 - 13:00	Lunch Break (Room A3+A4)
13:00 - 14:50	Session 1: Memory Devices (Room A1) 1 invited + 4 contributed
13:10 - 14:50	Session 2: Carrier Transport (Room A2) 5 contributed
14:50 - 15:20	Coffee Break
15:20 - 17:10	Session 3: Two-Dimensional Materials (Room A1) 1 invited + 4 contributed
15:30 - 17:10	Session 4: Numerical Methods and Algorithms (Room A2) 5 contributed
18:00 - 20:00	Reception (Main Hall Foyer)

Tuesday, September 29th	
9:00 - 11:50	Session 5: Atomistic/Ab-initio Modeling (Room A1) 1 invited + 6 contributed
9:30 - 11:30	Session 6: Process Simulation (Room A2) 5 contributed
11:30 - 13:00	Lunch Break (Room A3+A4)
13:00 - 14:00	Session 7: Circuit Simulation and Compact Modeling (Room A1) 3 contributed
13:00 - 14:00	Session 8: Quantum Devices (Room A2) 3 contributed
14:15 - 15:45	Castle Walking Session (Kumamoto Castle)
16:00 - 18:00	Poster Session (Room A3+A4)

Wednesday, September 30th	
9:00 - 11:30	Session 9: Advanced CMOS Devices (Room A1) 1 invited + 5 contributed
9:10 - 11:50	Session 10: Defects, Reliability and Variability (Room A2) 7 contributed
11:30 - 13:00	Lunch Break (Room A3+A4)
13:00 - 14:20	Session 11: Artificial Intelligence and Machine Learning (Room A1) 4 contributed
13:00 - 14:20	Session 12: Thermal Transport and Electro-Thermal Modeling (Room A2) 4 contributed
14:30 - 14:45	Closing (Room A1)